

Timing diagram for a D flip-flop showing the relationship between the clock signal (P) and the output signal ( $\bar{P}$ ). The diagram includes the following signals and timing parameters:

- SET**: A signal that transitions from low to high at the start of the clock pulse.
- P**: The clock signal, represented by a pulse with duration  $T$ .
- NSET**: A signal that transitions from high to low at the start of the clock pulse.
- PSET**: A signal that transitions from high to low at the start of the clock pulse.
- t1** and **t2**: Time intervals marking the start and end of the clock pulse.
- $\bar{P}$** : The output signal, which transitions from high to low during the clock pulse.

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